MICREX-SX How-to Guide

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How to Create Multi-CPU Project with SX-Programmer Standard

1. Overview

This guide describes how to create a project of multi-CPU configuration using SX-Programmer Standard. In SX-Programmer Standard, one project controls one resource. Therefore, for a multi-CPU system, you need to create projects as many as the CPUs to be used.

2. When creating a new project

This section describes how to create a new project using a multi-CPU system with CPU 0 (NP1PS-32R) and CPU 1 (NP1PM-48E) as an example.

2.1 Creating a template project for CPU 0

Execute the "New Project" command and select "SPH300-32." Then, open the system definition screen and add CPU 1.



On the "Module insert" dialog, set the CPU No. to "1", and select "Multi CPU" for the name and "NP1PM-48E" for the outline specification.

Module insert				×
	Circuit No:	Na <u>m</u> e[Resource name] : Multi CPU	I	ОК
Module <u>a</u> ttribute type		Outline specification:		Gancel
 Baseboard unit type module 		Туре	Outline specification	<u>P</u> arameter
C Individual type module C Block type module		NP1PM-256H NP1PM-48CR NP1PM-48R	SPH2000-256H SPH2000-48CR SPH2000-48B	<u>H</u> elp
C Board type module		NP1PM-48E	SPH2000-48E	insert position —_
Module group type		NP 1PS-117	High Performance CPU11	C Insert
C CPU	C Function	NP1PS-74	High Performance CPU74	 Addition
C Processor link	C Communication			
C Direct I/O	C Power	Туре:		🗖 No <u>e</u> quipment
C I/O master	C Baseboard	NP1PM-48E		
C Slave	Optical link	Consumed current(mA):		
C Remote I/O	🔿 Other	200		
	C Interface			
		1		

Register the other modules.



Close the system definition screen. The following dialog appears. Click the [No] button.



The following dialog appears. Click the [Yes] button.

System definition check information	X
Warning:The module/unit exists which is not allocated in I/O group setting.	
▲ These warnings exist. Exit system definition anyway?)
Yes <u>N</u> o	Memo

Execute the "Save As" command to save the project with a new name. The template project for CPU 0 has been created.

2.2 Creating a template project for CPU 1

Then, save the same project with a different name. This will become the template project for CPU 1. Next, change the PLC model to 48E of SPH2000.

<StandardV2>

Select PLC Model	×	
ELC Model	PLC Memory Size	
C NB0	○ 48K (NP1PM-48R)	
C NBE		
C NB	(NP1PM-48F)	
C NJE		
O NJ		
C NJH	C 256K (NP1PM-256E)	
C NS		
MICREX-SX Series		
C SPH200		
C SPH300		
C CPB (N mode)		
C SPB (SX mode)		
C Board Controller		
OK. Cancel	<u>H</u> elp	
	7	
Warning	×	Click the [No] button on this dialo
PLC type is changed. Do you wish to initialized Syste	m Definitions?	
Yes: System Definitions are init No: CPU Memory Size Definitio	alized. ns are initialized.	
Plance confirm the Sustem Def	witten ofter elements the PLC type	
Flease commit the system bein	nicion alter changing the FLC type.	

<StandardV3>



Do not check "Initialize system definitions."

Then, change CPU 0 to "Multi CPU." Right-click CPU 0 and open the "Module properties" dialog.



Change the name to "Multi CPU" and the outline specification to "NP1PS-32."

			-	
CPU No:	Gi <u>r</u> cuit No:	Na <u>m</u> e[Resource nam	ne]:	ОК
	<u>v</u>	Multi CPU		Canaal
Module <u>a</u> ttribute type —		Outline specification	¢	
 Baseboard unit type 	module	Туре	Outline specification 🔺	Parameter
C Individual type modu	le	NP1PM-48E	SPH2000-48E	
C Block type module		NP1PS-245	High Performance CPU24	
C Board type module		NP1PS-117	High Performance CPU11	
		NP1PS-74 NP1PS-74D	Expanded High Performat	
Module group type		NP1PS-32	High Performance CPU32	
OPU	C Function			
C Processor link	C Communication			
C Direct I/O	C Power	Type:		🔽 No <u>e</u> quipment
C I/O master	C Baseboard	NP1PS-32		
C Slave	🔿 Optical link	Consumed current(m	A):	
C Remote I/O	C Other	200		
	C Interface			

On the following dialog, click the [OK] button.



CPU 0 has been changed to "Multi CPU."

Then, change CPU 1 to "Resource." Right-click CPU 1 and open the "Module properties" dialog.



Change the name to "Resource."

lodule properties				
CPU <u>N</u> o;	Ci <u>r</u> cuit No:	Na <u>m</u> e[Resource name <mark>Resource</mark>	e] :	OK
Module <u>a</u> ttribute type —		Outline specification:		
 Baseboard unit type 	module	Туре	Outline specification	<u>P</u> arameter
C Individual type modul	le	NP1PU-048CE	SPH3000-048CE SPH2000-256E	Holp
C Block type module		NP1PM-256H	SPH2000-256H	
C Board type module		NP1PM-48CR	SPH2000-48CR	
		NP1PM-48E	SPH2000-48E	
Module group type		NP1PM-48ES	SPH2000-48ES	
 CPU 	C Function	NP1P5-245	High Performance CPU24	
C Processor link	C Communication		<u> </u>	
C Direct I/O	C Power	Туре:		📕 No <u>e</u> quipment
C I/O master	C Baseboard	NP1PM-48E		
C Slave	C Optical link	Consumed current(m	A):	
C Remote I/O	C Other	200		
	C Interface			

CPU 1 has been changed to "Resource."



Close the system definition screen. The following dialog appears. Click the [No] button.



The following dialog appears. Click the [Yes] button.



The template project for CPU 1 has been created.

2.3 Setting I/O group of CPU 0

Open the project for CPU 0, and then open the "CPU parameter" dialog in the system definition. Register I/O modules to assign to CPU 0 in Input select / Output select.

CPU parameter	
CPU running definition Memory allocation setting	I/O group setting Fail-soft operation setting
Level: DEFAULT	Standard setting
I/O <u>L</u> ist	Input select: Input for referring output value
 Direct I/O: SX station No1: DC/AC Input Direct I/O: SX station No2: DC/AC Input Direct I/O: SX station No3: Ry Output 16; Direct I/O: SX station No4: Ry Output 16; 	Direct I/O : SX station No1 : DC/AC Input 16p
	∢ ►
	Output select Detail
	Direct I/O : SX station No3 : Ry Output 16poin
	All cancel
	OK Cancel <u>H</u> elp

Close the system definition screen. The following dialog appears. Click the [No] button.



The following dialog appears. Click the [Yes] button.

System definition check information	×
Warning:The module/unit exists which is not allocated in I/O group setting.	
·	
These warnings exist. Exit system definition anyway?	Memo

2.4 Setting I/O group of CPU 1

In the same manner, open the project for CPU 1, and then open the "CPU parameter" dialog in the system definition. Register I/Os to assign to CPU 1 in Input select / Output select.

3. Special notes

Basically, the system definition settings such as the system property settings and module registration must be the same in both projects for CPU 0 and CPU 1.

For the name of the CPU, CPU 0 is called "Resource" and CPU 1 is called "Multi CPU" in the project for CPU 0 and CPU 1 is called "Resource" and CPU 0 is called "Multi CPU" in the project for CPU 1. (Self station = "Resource," The other station = "Multi CPU")



The following dialog appears every time you close the system definition screen. Be sure to click **[No]** button. If you click [Yes] button, all the I/O modules including the ones that are to be controlled by another CPU are assigned to the self CPU.



When you change the memory allocation, make the same change to both projects for CPU 0 and CPU 1.

<Memory allocation setting> [Project for CPU 0]

CPU 0 Memory allocation setting

OPU running definition	Memory allocation set	ting I/O group setting I	Memory allocation set
		Range of	Total Size : 96
<u>N</u> on retain memory	8.0 KW	WM0000000	_ Memory Size — <mark>64</mark>
<u>R</u> etain memory	4.0 KW	WL000000	Non retain m
User <u>F</u> B memory	4.0 KW		- Memory Size - 32
System FB memory	16.0 KW		Multi CPU n
ínitial data	3200	<u>D</u> efault	Retain mem
Detail of system Fi	B memory		Multi CPU n
<u>E</u> dge detection	1024 Point ×	2048 W	<u>U</u> ser FB me
<u>C</u> ounter	256 Point ×	: 4W 1024 W	<u>S</u> ystem FB
<u>A</u> dditional timer	128 Point ×	: 8W 1024 W	Initial data:
∐imer	512 Point x	: 8W 4096 W	Detail of sy
Other system FB	area	8192 W	<u>E</u> dge dete
			Additional
			Timer:
			Other sys
oject for CPU 1	Same	e setting	
oject for CPU 1 CPU 0 1	Same] Vemory allocation	e setting	CPU
oject for CPU 1 CPU 0 1 U parameter	Same] Memory allocation	e setting n setting	CPU CPU parameter
oject for CPU 1 CPU 0 1 U parameter Memory allocation set	Same] Vemory allocation	e setting	CPU CPU parameter CPU running definition
oject for CPU 1 CPU 0 1 U parameter Vemory allocation set	Same] Memory allocation ting	e setting	CPU CPU parameter CPU running definition Total Size : 96
oject for CPU 1 CPU 0 1 U parameter Memory allocation set Yon retain memory	Same	e setting a setting Range of WM0000000	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64
oject for CPU 1 CPU 0 I U parameter Memory allocation set Yon retain memory Betain memory	Same	e setting n setting Range of WM0000000 WL0000000	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m
oject for CPU 1 CPU 0 1 U parameter Memory allocation set Yon retain memory Betain memory Jser EB memory	Same	e setting a setting Range of WM0000000 WL0000000	CPU CPU parameter CPU running definition Total Size : 36 Memory Size - 64 Non retain m
Oject for CPU 1 CPU 0 1 U parameter Memory allocation set Mon retain memory Betain memory Jser <u>E</u> B memory System FB memory	Same	e setting n setting Range of WM0000000 WL0000000	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m Memory Size - 32 Multi CPU n
Oject for CPU 1 CPU 0 1 U parameter Memory allocation set Mon retain memory Retain memory Jser <u>FB</u> memory System FB memory initial data	Same	e setting Range of WM0000000 WL00000000 WL00000000	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m Memory Size - 32 Multi CPU n <u>R</u> etain mem
Oject for CPU 1 CPU 0 1 U parameter Memory allocation set Non retain memory Betain memory User EB memory System FB memory hitial data	Same	e setting a setting Range of WM0000000 WL0000000 Default	CPU CPU parameter CPU running definition Total Size : 966 Memory Size - 64 Non retain m Memory Size - 32 Multi CPU n Betain mem Multi CPU n
Oject for CPU 1 CPU 0 I U parameter Memory allocation set Mon retain memory Betain memory User EB memory System FB memory Ditial data Detail of system FI Edge detection	Same	e setting Range of WM0000000 WL000000 WL000000 WL000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL00000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL0000 WL00000 WL00000 WL0000	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m Memory Size - 32 Multi CPU n <u>R</u> etain mem Multi CPU n <u>U</u> ser FB me
Oject for CPU 1 CPU 0 1 U parameter Memory allocation set Mon retain memory Betain memory User EB memory System FB memory hitial data Detail of system FI Edge detection Qounter	Same	e setting Range of WM0000000 WL000000 WL000000 WL000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL00000 WL000000 WL00000 WL000000 WL000000 WL00000 WL000000 WL000000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL0000 WL00000 WL00000 WL0000 WL00000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL000 WL0000 WL000	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m Memory Size - 92 Multi CPU n Retain mem Multi CPU n User FB me System FB
Oject for CPU 1 CPU 0 I U parameter Memory allocation set Mon retain memory Betain memory User EB memory System FB memory Ditial data Detail of system FI Edge detection Qounter Additional timer	Same	e setting a setting Range of WM0000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL00000 WL000000 WL000000 WL00000 WL000000 WL00000 WL00000 WL00000 WL000000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL00000 WL0000 WL0000 WL00000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m Memory Size - 32 Multi CPU m Betain mem Multi CPU m User FB me System FB Initial data:
oject for CPU 1 CPU 0 I U parameter Memory allocation set Mon retain memory Betain memory User EB memory System FB memory Initial data Detail of system FI Edge detection Qounter Additional timer Timer	Same] Memory allocation tine 8.0 KW 4.0 KW 4.0 KW 3200 B memory 1024 Point × 256 Point × 128 Point × 512 Point ×	e setting a setting Range of WM0000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL00000 WL000000 WL000000 WL00000 WL00000 WL00000 WL00000 WL000000 WL000000 WL00000 WL00000 WL00000 WL0000 WL00000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL0000 WL	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m Memory Size - 322 Multi CPU n Retain memu Multi CPU n Betain memu System FB Initial data: Detail of sy
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oject for CPU 1 CPU 0 I U parameter Memory allocation set Mon retain memory Retain memory User EB memory System FB memory System FB memory Initial data Detail of system FB Edge detection Qounter Additional timer Timer Other system FB	Same	e setting Range of WM0000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL0000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL000000 WL00000 WL000000 WL0000 WL000 WL0000 WL000 WL0000 WL000 WL0000 WL0000 WL0000 WL000 WL00	CPU CPU parameter CPU running definition Total Size : 96 Memory Size - 64 Non retain m Multi CPU n Betain mem Multi CPU n User FB me System FB Initial data: Detail of sy Edge dete <u>C</u> ounter: <u>A</u> dditional

CPU 1 Memory allocation setting

lemory	allocation setting			
Total	Size : 96.0 KW			
Memo	ory Size -64.0 -KW			
	Non retain memory:	64.0	K₩	
- Memo	ory Size - 32.0 + KW			Auto
	Multi CPU non retain memory:	0.0	КW	(B)
	Retain memory:	8.0	ĸw	(F)
	Multi CPU retain memory:	0.0	KW	(G)
	User FB memory:	8.0	KW	O (J)
	System FB memory:	16.0	КW	• (K)
	Initial data: Detail of system FB memory	12800		
	Edge detection: 1024	Point x 2W	2048	- w
	Counter: 256	Point x 4W	1024	
	Additional timer: 128	Point × 8W	1024	w
		Point x 8W	4096	W
	Other system FB area:		8192	W
L				•
				Λ
	CPU 1 Memory alloca	Same so	etting a	
U para	CPU 1 Memory alloca	Same settion settin	etting g	
U para PU run Total	CPU 1 Memory alloca meter ning definition Memory allocation se Size : 96.0 KW	Same settion settin	etting g oup sett	V ing Fail-
Upara PUrun Total	CPU 1 Memory alloca meter ning definition Memory allocation se Size : 96.0 KW ory Size - 64.0 - KW	Same so tion settin	etting g	V ing Fail-
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Upara PUrun Total Memo	CPU 1 Memory alloca meter ning definition Memory allocation se Size : 96.0 KW ory Size - 64.0 - KW Non retain memory: Dry Size - 82.0 - KW Multi CPU non retain memory:	Same settin tion settin ettine 1/0 ero	etting g pup sett KW	Auto
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Upara PUrun Total Memo	CPU 1 Memory alloca meter ning definition Memory allocation se Size : 96.0 KW ory Size -64.0 - KW Non retain memory: ory Size -92.0 ÷ KW Multi CPU non retain memory: Retain memory: Multi CPU retain memory: User FB memory: System FB memory:	Same settin tion settin ettine 1/0 ero 64.0 0.0 8.0 0.0 8.0 16.0	g g kw kw kw kw kw kw kw	Auto C (B) C (G) C (J) C (X)
Upara PUrun Total Memo	CPU 1 Memory alloca meter ning definition Memory allocation se Size : 96.0 KW ory Size -64.0 - KW Non retain memory: Size -32.0 - KW Multi CPU non retain memory: Betain memory: Multi CPU retain memory: User FB memory: System FB memory: Initial data: Detail of system FB memory.	Same settin tion settin ttine 1/0 ero 64.0 64.0 0.0 8.0 16.0 12800	9 9 KW KW KW KW KW KW	V Auto C (B) C (C) C (G) C (J) C (J) C (J) C (J)
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Upara PUrun Total Memo	CPU 1 Memory alloca Imeter ning definition Memory allocation set Size : 96.0 KW ory Size - 64.0 - KW Non retain memory: - KW ory Size - 82.0 - KW Multi CPU non retain memory: Betain memory: Multi CPU retain memory: Lyser FB memory: System FB memory: Initial data: Detail of system FB memory Edge detection: 1024 Qounter: 256 Additional timer: 128 Timer: 512	Same so tion settin ttine I/O ero 64.0 64.0 0.0 8.0 16.0 12800 Point x 2W Point x 4W Point x 8W	etting 9 Sup sett KW KW KW KW KW KW KW 2048 1024 1024 4096	V Auto
Upara PUrun Total Memo	CPU 1 Memory alloca meter ning definition Memory allocation set Size : 96.0 KW ory Size 64.0 KW Non retain memory: ory Size 82.0 KW Multi CPU non retain memory: Betain memory: Multi CPU retain memory: User FB memory: System FB memory: Initial data: Detail of system FB memory Edge detection: 1024 Qounter: 256 Additional timer: 128 Timer: 512 Other system FB area:	Same so tion settin ttine 1/0 ero 64.0 64.0 0.0 8.0 16.0 12800 Point x 2W Point x 4W Point x 8W	etting g g kW kW kW kW kW kW kW kW kW kW kW kW kW	

Assign programs to a task in the project for CPU 0 and in the project for CPU 1 respectively.











Assign the programs for CPU 0 to a task.

[Project for CPU 1]



Assign the programs for CPU 1 to a task.

[Project for CPU 1]



Assign the programs for CPU 1 to a task.

4. When changing the project

When changing system definition or memory allocation, the projects of all the CPUs must be changed in the same way. After changing the projects, download the program and system definition to all the CPUs and reset the entire system. When changing only a program without changing system definition or memory allocation, you do not need to change the projects of all the CPUs. Change only the program of the target CPU.

When changing memory allocation by uploading the project from a CPU, read the following instructions.

4.1 Correct procedure

- 1) Upload the projects from each CPU and save them individually in a file with a new name for each CPU.
- 2) Close the online screen and open the project file saved in the step 1) in offline mode.
- 3) Change the memory allocation. Make the same change to all the project files.
- 4) Stop all the CPUs and download the changed project files individually to each CPU. After the download is completed, reset the entire system.

4.2 Incorrect procedure

1) The project is uploaded from CPU 0. After memory allocation is changed, the project is downloaded to CPU 0.

- 2) The project is uploaded from CPU 1. After memory allocation is changed, the project is downloaded to CPU 1.
- 3) The entire system is reset.
- * Do not execute "upload" operation and "project change" operation alternately as follows: "Upload" → "Project change" → "Upload" → "Project change"

Upload the projects from all the CPUs first, change them in offline mode, and then download them individually to each CPU. If you follow the incorrect procedure, an address that is different from the one specified in the program may be accessed during data communication between the CPUs.